

INTERNATIONAL SEARCH REPORT

Internal Application No
PCT/IB2004/051443

A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 H04L25/14 H04L7/04

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H04L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, INSPEC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	RICH TABOREK: "Hari Coding Objectives" INTERNET ARTICLE, 'Online! XP002310888 Retrieved from the Internet: URL: http://grouper.ieee.org/groups/802/3/10G_study/public/nov99/taborek_1_1199.pdf * Slides 1,2,6,17 *	1-35
X	US 5 598 442 A (CAPOWSKI ROBERT S ET AL) 28 January 1997 (1997-01-28) abstract column 2, line 31 - line 50; figure 1 column 3, line 19 - line 64; figures 2,3 ----- -/--	1-35

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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- *A* document defining the general state of the art which is not considered to be of particular relevance
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- *Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.
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Date of the actual completion of the international search

15 December 2004

Date of mailing of the international search report

29/12/2004

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	<p>FUKAISHI M ET AL: "A 4.25 Gb/s CMOS fiber channel transceiver with asynchronous binary tree-type demultiplexer and frequency conversion architecture" SOLID-STATE CIRCUITS CONFERENCE, 1998. DIGEST OF TECHNICAL PAPERS. 1998 IEEE INTERNATIONAL SAN FRANCISCO, CA, USA 5-7 FEB. 1998, NEW YORK, NY, USA, IEEE, US, 5 February 1998 (1998-02-05), pages 306-307, 452, XP010278587 ISBN: 0-7803-4344-1 page 1, right-hand column, paragraph 2; figure 5</p> <p>-----</p>	<p>1,12,19, 22,26,32</p>

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Information on patent family members

Inter 1al Application No

PCT/IB2004/051443

Patent document cited in search report		Publication date		Patent family member(s)	Publication date
US 5598442	A	28-01-1997	US	5651033 A	22-07-1997